



RF and mm-Wave Power Generation in Silicon

From Academic Press



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This book presents the challenges and solutions of designing power amplifiers at RF and mm-Wave frequencies in a silicon-based process technology. It covers practical power amplifier design methodologies, energy- and spectrum-efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications, and power amplifier and power generation designs for enabling new communication and sensing applications in the mm-Wave and THz frequencies.

With this book you will learn:

- Power amplifier design fundamentals and methodologies
- Latest advances in silicon-based RF power amplifier architectures and designs and their integration in wireless communication systems
- State-of-the-art mm-Wave/THz power amplifier and power generation circuits and systems in silicon
- Extensive coverage from fundamentals to advanced design topics, focusing on various layers of abstraction: from device modeling and circuit design strategy to advanced digital and mixed-signal architectures for highly efficient and linear power amplifiers
- New architectures for power amplifiers in the cellular and wireless connectivity covering detailed design methodologies and state-of-the-art performances
- Detailed design techniques, trade-off analysis and design examples for efficiency enhancement at power back-off and linear amplification for spectrally-efficient non-constant envelope modulations
- Extensive coverage of mm-Wave power-generation techniques from the early days of the 60 GHz research to current state-of the-art reconfigurable, digital mm-Wave PA architectures
- Detailed analysis of power generation challenges in the higher mm-Wave and THz frequencies and novel technical solutions for a wide range for potential applications, including ultrafast wireless communication to sensing, imaging and spectroscopy
- Contributions from the world-class experts from both academia and industry

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Editorial Review

From the Back Cover

Gain insight into the fundamentals of RF and mm-Wave silicon-based power amplifiers and transmitters, and learn novel design techniques for implementation in state-of-the-art IC processes

This book presents the challenges and solutions of designing power amplifiers at RF and mm-Wave frequencies in a silicon-based process technology. It covers practical power amplifier design methodologies, energy- and spectrum-efficient power amplifier design examples in the RF frequency for cellular and wireless connectivity applications, and power amplifier and power generation designs for enabling new communication and sensing applications in the mm-Wave and THz frequencies.

Key features:

- Extensive coverage from fundamentals to advanced design topics, focusing on various layers of abstraction: from device modeling and circuit design strategy to advanced digital and mixed-signal architectures for highly efficient and linear power amplifiers
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With this book you will learn:

State-of-the-art mm-Wave/THz power amplifier and power generation circuits and systems in silicon. About the Author

Hua Wang Hua Wang (M'05?SM'15) received his B.S. degree from Tsinghua University, Beijing, China, in 2003, and M.S. and Ph.D. degrees in electrical engineering from the California Institute of Technology, Pasadena, in 2007 and 2009, respectively. He was with Guidant Corporation during the summer of 2004, working on accelerometer-based posture monitoring systems for implantable biomedical devices. In 2010, he joined Intel Corporation, where he worked on the next generation energy-efficient mm-wave communication link and broadband CMOS Front-End-Module for Wi-Fi systems. In 2011, he joined Skyworks Solutions. His work at Skyworks included the development of SAW-less integrated filter solutions for low-cost cellular-standard Front-End-Module. In 2012, he joined the School of Electrical and Computer Engineering at Georgia Institute of Technology as an assistant professor. He currently holds the Demetrios T. Paris Junior Professorship of the School of Electrical and Computer Engineering. He is generally interested in innovating mixed-signal, RF, and mm-Wave integrated circuits and systems for communication, radar, and

bioelectronics applications. Dr. Wang received National Science Foundation (NSF) CAREER Award in 2015, Roger P. Webb ECE Outstanding Junior Faculty Member Award in 2015, and Lockheed Martin Dean's Excellence in Teaching Award in 2015. He was the award recipient of the 46th IEEE DAC/ISSCC Student Design Contest Winner in 2009 based on his work of "An Ultrasensitive CMOS Magnetic Biosensor Array for Point-Of-Care (POC) Microarray Application." He was also a co-recipient of the IEEE Radio Frequency Integrated Circuits Symposium (RFIC) Best Student Paper Award (1st Place) as the students' Ph.D. advisor in 2014. Dr. Wang is an Associate Editor of the IEEE Microwave and Wireless Components Letters (MWCL). He is currently a Technical Program Committee (TPC) Member for IEEE Radio Frequency Integrated Circuits Symposium (RFIC), IEEE Custom Integrated Circuits Conference (CICC), IEEE Bipolar/BiCMOS Circuits and Technology Meeting (BCTM), and IEEE/CAS-EMB Biomedical Circuits and Systems Conference (BioCAS). He serves as the Chair of the Atlanta's IEEE CAS/SSCS joint chapter, which won the IEEE SSCS Outstanding Chapter Award in 2014.

Kaushik Sengupta Kaushik Sengupta (M'12) received the B.Tech. and M.Tech. degrees in electronics and electrical communication engineering from the Indian Institute of Technology (IIT), Kharagpur, India, both in 2007, and the M.S. and Ph.D. degrees in electrical engineering from the California Institute of Technology, Pasadena, CA, USA, in 2008 and 2012, respectively. In February 2013, he joined the faculty of the Department of Electrical Engineering, Princeton University, Princeton, NJ, USA. During his undergraduate studies, in the summers of 2005 and 2006, he performed research at the University of Southern California and the Massachusetts Institute of Technology (MIT), where he was involved with nonlinear integrated systems for high-purity signal generation and low-power RF identification (RFID) tags, respectively. His research interests are in the areas of high-frequency integrated circuits (ICs), electromagnetics, optics for various applications in sensing, imaging, and high-speed communication. Dr. Sengupta was the recipient of the IBM Ph.D. fellowship (2011-2012), the IEEE Solid-State Circuits Society Predoctoral Achievement Award (2012), the IEEE Microwave Theory and Techniques Graduate Fellowship (2012), and the Analog Devices Outstanding Student Designer Award (2011). He was the recipient of the Charles Wilts Prize in 2013 from Electrical Engineering, Caltech for outstanding independent research in electrical engineering leading to a PhD. He was also the recipient of the Prime Minister Gold Medal Award of IIT (2007), the Caltech Institute Fellowship, the Most Innovative Student Project Award of the Indian National Academy of Engineering (2007), and the IEEE Microwave Theory and Techniques Undergraduate Fellowship (2006). He serves on the Technical Program Committee of the European Solid-State Circuits Conference (ESSCIRC). He was selected in "Princeton Engineering Commendation List for Outstanding Teaching" in 2014. He was the co-recipient of the IEEE RFIC Symposium Best Student Paper Award in 2012 and 2015 IEEE Microwave Theory and Techniques Society (IEEE MTT-S) Microwave Prize. **Users Review** **From reader reviews:**

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