



# Digital Circuit Testing: A Guide to DFT and Other Techniques

*By Francis C. Wang*

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Recent technological advances have created a testing crisis in the electronics industry--smaller, more highly integrated electronic circuits and new packaging techniques make it increasingly difficult to physically access test nodes. New testing methods are needed for the next generation of electronic equipment and a great deal of emphasis is being placed on the development of these methods. Some of the techniques now becoming popular include design for testability (DFT), built-in self-test (BIST), and automatic test vector generation (ATVG). This book will provide a practical introduction to these and other testing techniques. For each technique introduced, the author provides real-world examples so the reader can achieve a working knowledge of how to choose and apply these increasingly important testing methods.

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## **Editorial Review**

### **Review**

"The text, **Digital Circuit Testing: A Guide to DFT and Other Techniques**, introduces the reader to the whole spectrum of digital test technology, covering some facets in more detail than others....This book is intended to be an introduction to straight-forward testing techniques, easily applicable to the daily work of design and test engineers or as a course or reference text. The author accomplishes his purpose succinctly and in an easy-to-understand style."

--IEEE, INSTRUMENTATION AND MEASUREMENT SOCIETY NEWSLETTER

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