



Digital Signal Processing Implementations: Using DSP Microprocessors (with examples from TMS320C54XX)

By Avtar Singh, S. Srinivasan

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Editorial Review

Review

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About the Author

Avtar Singh is Professor of Electrical Engineering at San Jose State University. Earlier he taught at the City University of New York and the County College of Morris. Before coming to San Jose State University, he was with industry. He has worked for National semiconductor, Anderson Jacobson, and Vivix Corporation, all in the silicon-valley. At San Jose State, Dr. Singh is involved in teaching and research in the areas of DSP implementation, biomedical instrumentation, and programmable devices and processors. He has published a

number of articles in his areas of interest. He has also co-authored nine textbooks on Microprocessors.

S. Srinivasan is currently a Professor in the Electrical Engineering Department at the Indian Institute of Technology. In 1998-1999, he was a Visiting Professor at California State University, where he worked as Associate Professor from 1986-1990. His teaching areas include Digital Circuits and Systems, Computer Architecture, and VLSI Design. His research areas are Architectures and Applications of Digital Signal Processors, Image Processing Implementations, Video Compression, and ASIC Design. Dr. Srinivasan's awards and fellowships include the Siemens Prize for Academic Proficiency (1970), DAAD Fellowship (1977-78), Alexander von Humboldt Fellowship (1983, not utilized), and the Best Design Entry award in the Design Contest held as part of the 13th International Conference on VLSI Design (2000).

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