



# Switching Theory and Logic Design

By A. Anand Kumar

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## Switching Theory and Logic Design By A. Anand Kumar

This comprehensive text on switching theory and logic design is designed for the undergraduate students of electronics and communication engineering, electrical and electronics engineering, electronics and instrumentation engineering, telecommunication engineering, computer science and engineering, and information technology. It will also be useful to AMIE, IETE and diploma students. Written in a student-friendly style, this book, now in its Second Edition, provides an in-depth knowledge of switching theory and the design techniques of digital circuits. Striking a balance between theory and practice, it covers topics ranging from number systems, binary codes, logic gates and Boolean algebra to minimization using K-maps and tabular method, design of combinational logic circuits, synchronous and asynchronous sequential circuits, and algorithmic state machines. The book discusses threshold gates and programmable logic devices (PLDs). In addition, it elaborates on flip-flops and shift registers. Each chapter includes several fully worked-out examples so that the students get a thorough grounding in related design concepts. Short questions with answers, review questions, fill in the blanks, multiple choice questions and problems are provided at the end of each chapter. These help the students test their level of understanding of the subject and prepare for examinations confidently. NEW TO THIS EDITION\* VHDL programs at the end of each chapter \* Complete answers with figures \* Several new problems with answers.

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#### **About the Author**

A. Anand Kumar, PhD, is Principal, K.L. University College of Engineering, K.L. University, Vijayawada, Andhra Pradesh, India. Earlier from 2006 to 2011 he served as Director, Sasi Institute of Technology and Engineering, Tadepalligudem, West Godavari District, Andhra Pradesh, India. From 2000 to 2006 he served as Principal, Sir C.R. Reddy College of Engineering, Eluru, West Godavari District, Andhra Pradesh, India, and as Professor, S.D.M. College of Engineering and Technology, Dharwad, Karnataka, India from 1983 to 2000. With more than 38 years of teaching experience, he has published five more books (All published by PHI Learning). His area of research includes radar signal processing.

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